

EXPERT'S PROFILE



NAME OF GRANTEE : **Ramon A. Cruz**
Area of Expertise : Wireless Communication / Electrical Engineering
Inclusive Date of Contract as BSP Awardee : Nov. 07, 2012 – Feb. 25, 2013
Short Term Program
Host Institution : Electrical and Electronics Engineering Institute,
University of the Philippines Diliman
Contact Number : +(63) 926 669 7855
E-mail Address : ramon.cruz@stanfordalumni.org

EDUCATIONAL BACKGROUND

- **Engineers Degree in Electrical Engineering**, Stanford University, 1999
- **M. Sc. in Electrical Engineering and Computer Science**, University of Illinois, 1987
- **B. Sc. in Electrical Engineering**, University of the Philippines, 1981

WORK EXPERIENCES

- Feb 2003 – Sep 2009 Principal Engineer
SiRF Technology Inc., San Jose, CA
- Jan 2002 – Jan 2003 Principal Engineer
Bermai Inc., Palo Alto, CA
- Nov 2000 – Oct 2001 Staff Engineer
Atoga Systems Inc., Fremont, CA
- July 1998 – Oct 2000 Channel coding Group Design Manager
LSI Logic Corp., Milpitas, CA
- July 1997 – June 1998 ARM Staff Applications Engineer
OAK, Carmel ARM Staff Application Engineer
- Aug 1995 – June 1997 Technical Staff Member
SiRF Technology Inc., Santa Clara, CA
- Oct 1994 – July 1994 Senior Engineering Specialist
Stanford Telecommunications, Sunnyvale, CA
- July 1992 – Oct 1994 Senior Design Engineering
LSI Logic Corp., Milpitas, CA
- Oct 1990 – June 1992 Senior Applications Engineer
LSI Logic Corp., Milpitas, CA
- April 1998 – Sept 1990 Product Engineer
LSI Logic Corp., Milpitas, CA

TO BE ACCOMPLISHED AS BSP AWARDEE

1. FPGA based Wireless implementation Design Methodology document.
2. Implementation of radar signal processing algorithms on FPGA-based platforms.
3. Establishment of a rapid prototyping capability of wireless systems through FPGA based platforms within the UP EEEL Wireless Communications Engineering Laboratory.
4. Proposed plan for establishing this capability in other Wireless/Digital Communication related research groups in the Philippines.